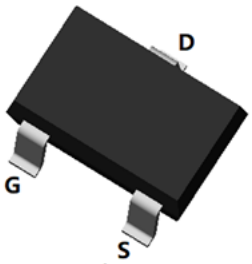
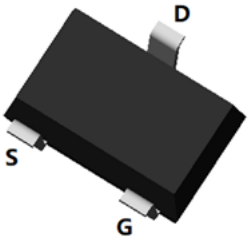
P-Channel Enhancement Mode Field Effect Transistor

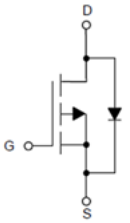


Top View



Bottom View

SOT-323



Product Summary

- V_{DS} -60 V
- I_D -0.17 A
- $R_{DS(ON)}$(at V_{GS}=-10V) ＜8 ohm
- $R_{DS(ON)}$(at V_{GS}=-4.5V) ＜9.9 ohm

General Description

- Trench Power LV MOSFET technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Video monitor
- Power management

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Maximum	Unit
Drain-source Voltage		V_{DS}	-60	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current	T_A=25℃ @ Steady State	I_D	-0.17	A
	T_A=70℃ @ Steady State		-0.14	
Pulsed Drain Current [A]		I_{DM}	-0.68	A
Total Power Dissipation @ T_A=25℃		P_D	0.15	W
Thermal Resistance Junction-to-Ambient [B]		$R_{\theta JA}$	833	℃/ W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■ Ordering Information

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
BSS84W	F2	B84.	3000	30000	120000	7“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=-250μA	-60			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-60V,V_{GS}=0V			-1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}= ±20V, V_{DS}=0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=-250μA	-0.9	-1.4	-2.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= -10V, I_D=-0.15A		3.3	8	Ω
		V_{GS}= -4.5V, I_D=-0.15A		3.5	9.9	
Diode Forward Voltage	V_{SD}	I_S=-0.17A,V_{GS}=0V			-1.2	V
Maximum Body-Diode Continuous Current	I_S				-0.17	A
Dynamic Parameters						
Input Capacitance	C_{iss}	V_{DS}=-30V,V_{GS}=0V,f=1MHZ		43		pF
Output Capacitance	C_{oss}			2.9		
Reverse Transfer Capacitance	C_{rss}			1.8		
Switching Parameters						
Total Gate Charge	Q_g	V_{GS}=-10V,V_{DS}=-30V,I_D=-0.15 A		1.77		nC
Gate Source Charge	Q_{gs}			0.57		
Gate Drain Charge	Q_{gd}			0.18		
Reverse Recovery Charge	Q_{rr}	IF=-0.15A, di/dt=100A/μs		13		
Reverse Recovery Time	t_{rr}			23		ns
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=-4.5V, V_{DD}=-30V, I_D=-0.15A, R_{GEN}=2.5Ω		8.6		
Turn-on Rise Time	t_r			20		
Turn-off Delay Time	$t_{D(off)}$			15		
Turn-off Fall Time	t_f			77		

A. Pulse Test: Pulse Width≤10us,Duty cycle ≤2%.
B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

■ Typical Performance Characteristics

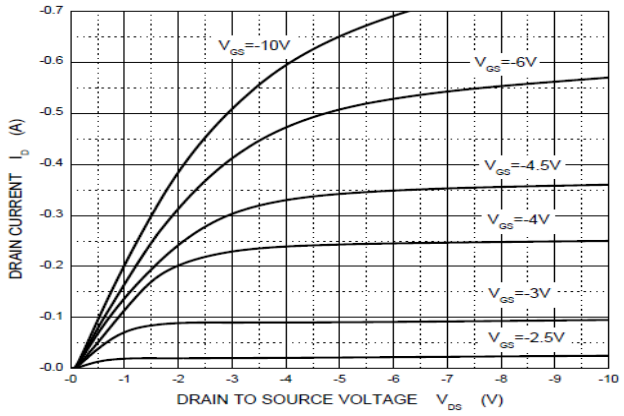


Figure1. Output Characteristics

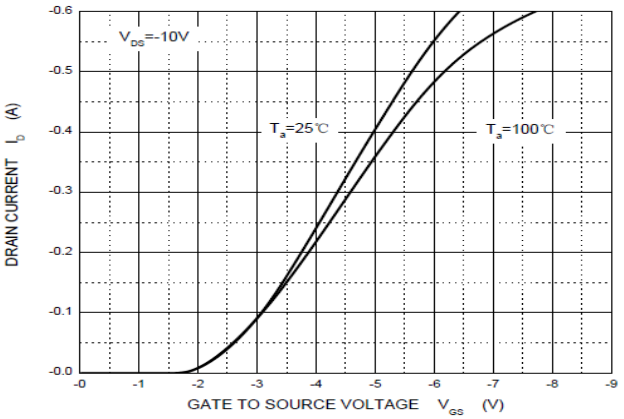


Figure2. Transfer Characteristics

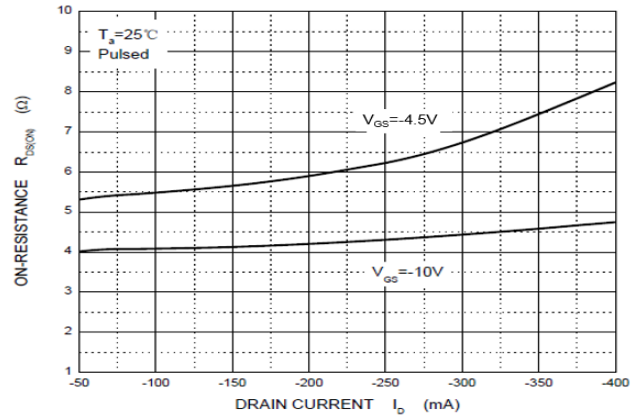


Figure3. Drain-Source on Resistance

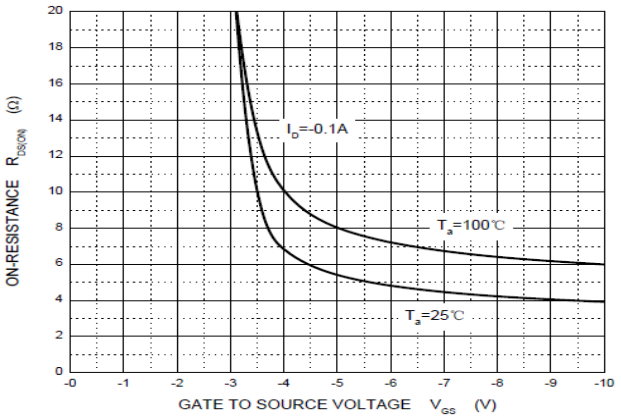


Figure4. Drain-Source on Resistance

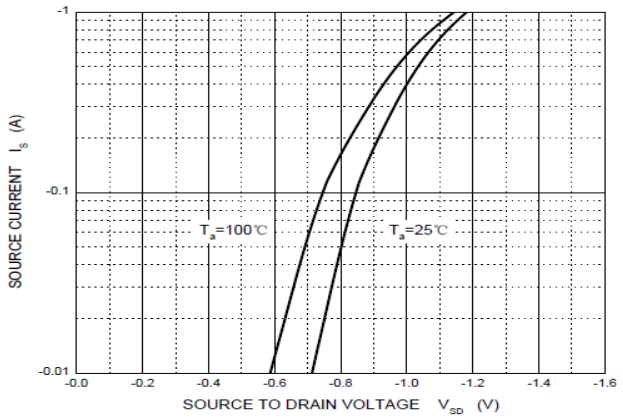


Figure5. Diode Forward Voltage vs. current

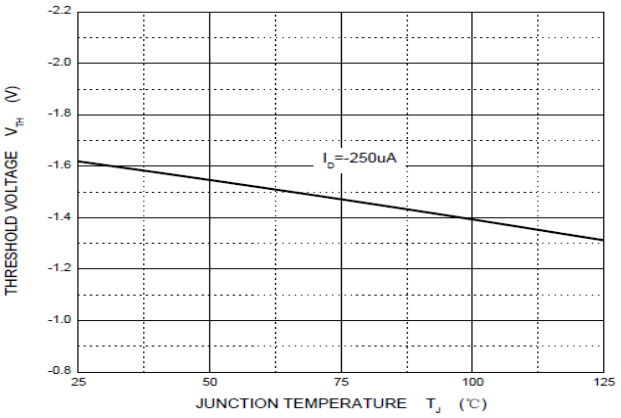


Figure6. Gate Threshold vs. Junction Temperature

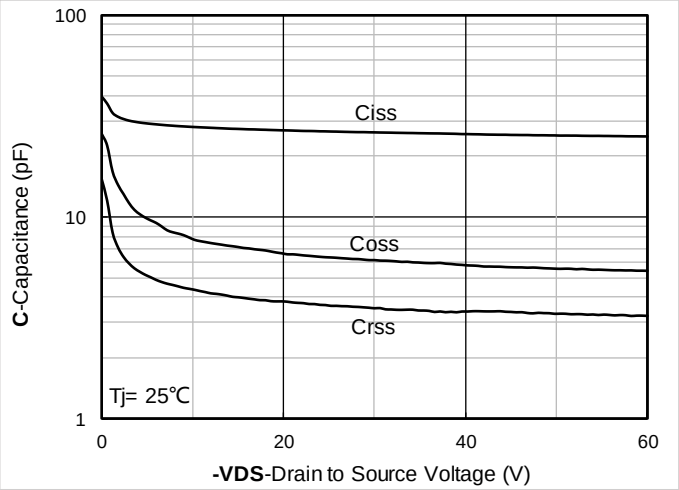


Figure7. Capacitance Characteristics

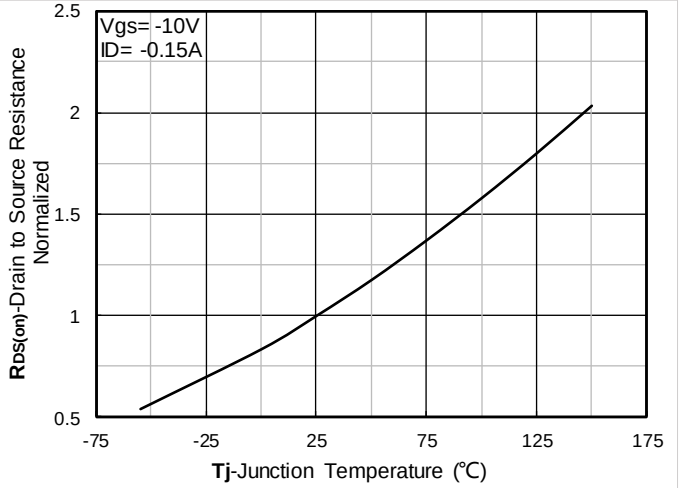
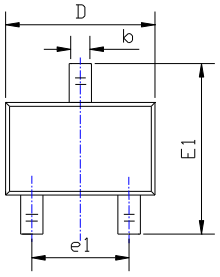
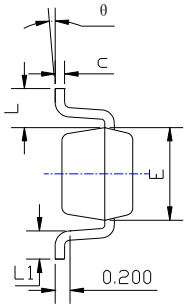


Figure8. Normalized On-Resistance

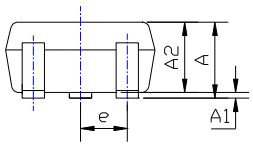
■ SOT-323 Package Information



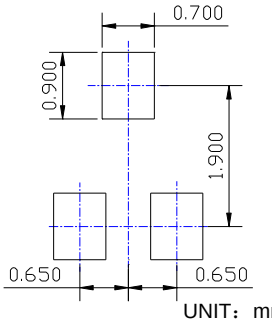
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT：mm

SUGGESTED SOLDER PAD LAYOUT

DIMENSIONS				
SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.016	0.150	0.400
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.096	2.150	2.450
e	0.026TYP		0.650TYP	
e1	0.047	0.055	1.200	1.400
L	0.021REF		0.525REF	
L1	0.010	0.018	0.260	0.460
θ	0°	8°	0°	8°

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

Notice

All products, product specifications and data are subject to change without notice to improve reliability, function or design or otherwise. Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in STARSEA's terms and conditions of sale for such products, STARSEA assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of STARSEA products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.