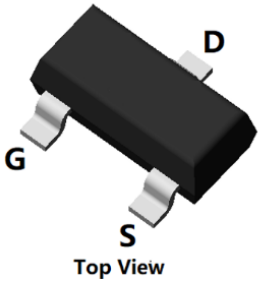
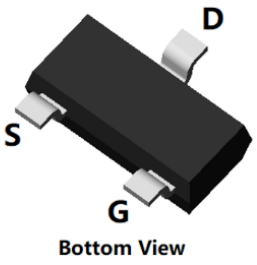
P-Channel Enhancement Mode Field Effect Transistor

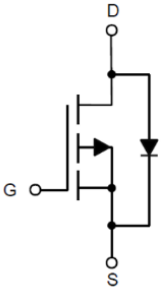


Top View



Bottom View

SOT-23



Product Summary

- V_{DS} -19V
- I_D -3.8A
- $R_{DS(ON)}$(at V_{GS}=-4.5V) ＜47mohm
- $R_{DS(ON)}$(at V_{GS}=-2.5V) ＜63mohm
- $R_{DS(ON)}$(at V_{GS}=-1.8V) ＜107mohm

General Description

- Trench Power LV MOSFET technology
- Low RDS(ON)
- Low Gate Charge
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- PWM applications
- Power management
- Load switch

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	-19	V
Gate-source Voltage		V_{GS}	±10	V
Drain Current	T_A=25℃	I_D	-3.8	A
	T_A=70℃		-3	
Pulsed Drain Current [A]		I_{DM}	-15	A
Total Power Dissipation	T_A=25℃	P_D	1	W
	T_A=70℃		0.64	W
Thermal Resistance Junction-to-Ambient [B]		$R_{\theta JA}$	125	℃/ W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
L2301D	F2	2301D.	3000	30000	120000	7“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250μA	-19			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-19V,V_{GS}=0V			-1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±10V, V_{DS}=0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=-250μA	-0.4	-0.62	-1.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=-4.5V, I_D=-3.8A		36	47	mΩ
		V_{GS}=-2.5V, I_D=-3.0A		48	63	
		V_{GS}=-1.8V, I_D=-2.5A		78	107	
Diode Forward Voltage	V_{SD}	I_S=-3.8A,V_{GS}=0V			-1.2	V
Maximum Body-Diode Continuous Current	I_S				-3.8	A
Dynamic Parameters						
Input Capacitance	C_{iss}	V_{DS}=-10V,V_{GS}=0V,f=1MHZ		606		pF
Output Capacitance	C_{oss}			114		
Reverse Transfer Capacitance	C_{rss}			103		
Switching Parameters						
Total Gate Charge	Q_g	V_{GS}=-10V,V_{DS}=-10V,I_D=-3.8A		8.48		nC
Gate-Source Charge	Q_{gs}			1.54		
Gate-Drain Charge	Q_{gd}			2.61		
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=-4.5V, V_{DS}=-10V, I_D=-3.8A R_{GEN}=3Ω		5.8		
Turn-on Rise Time	t_r			34.8		
Turn-off Delay Time	$t_{D(off)}$			51.4		
Turn-off fall Time	t_f			52		

A. Pulse Test: Pulse Width≤300us,Duty cycle ≤2%.

B. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.

■ Typical Performance Characteristics

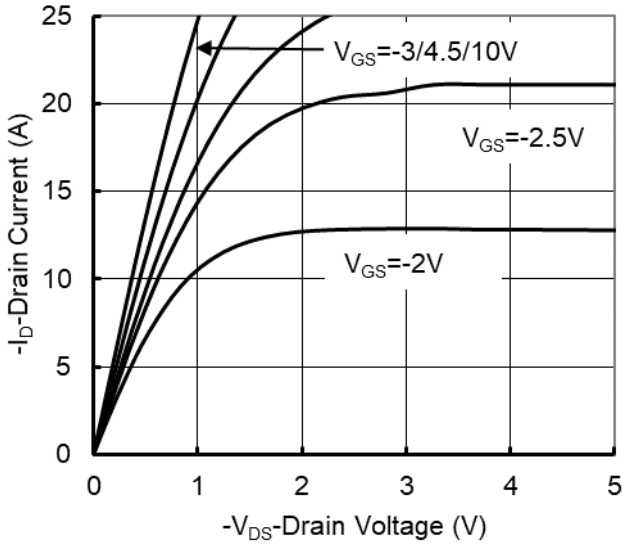


Figure1. Output Characteristics

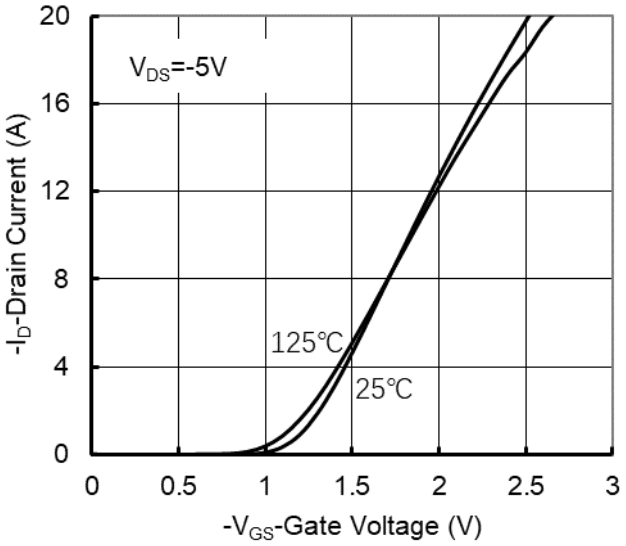


Figure2. Transfer Characteristics

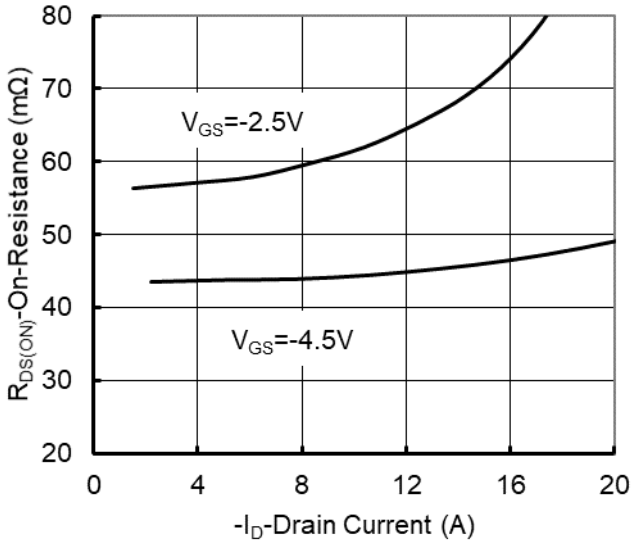


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

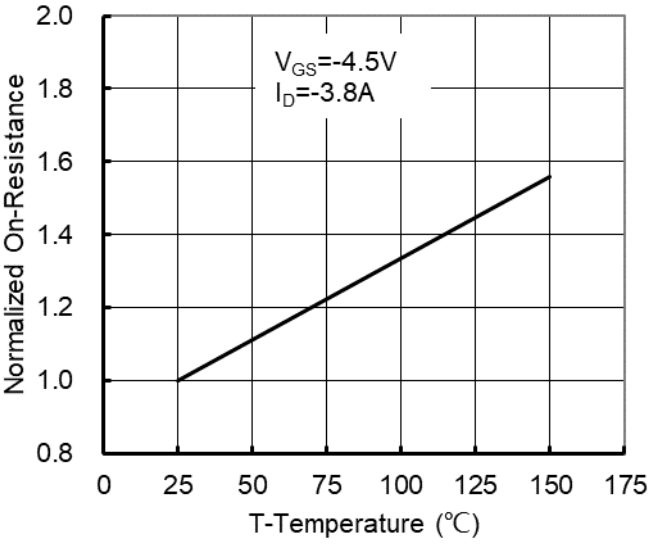


Figure 4: On-Resistance vs. Junction Temperature

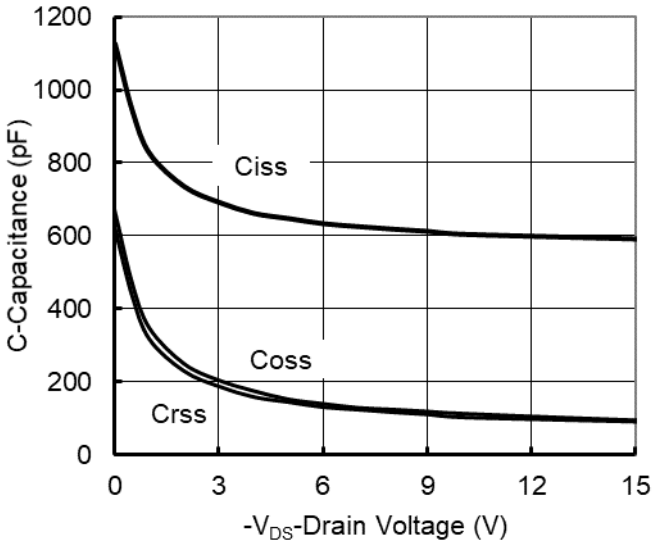


Figure5. Capacitance Characteristics

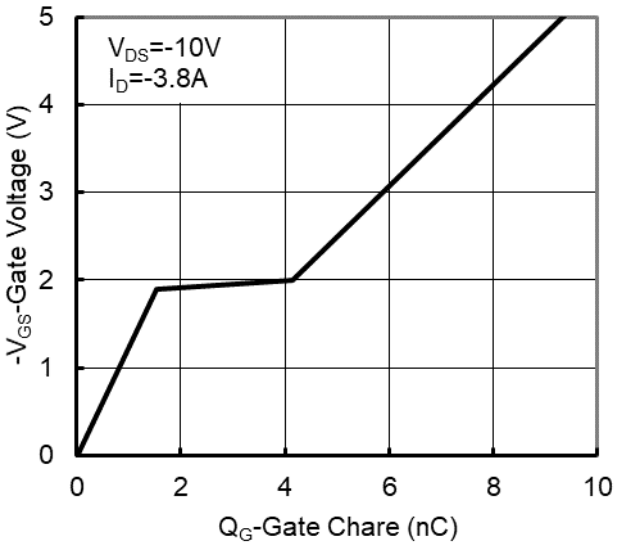


Figure6. Gate Charge

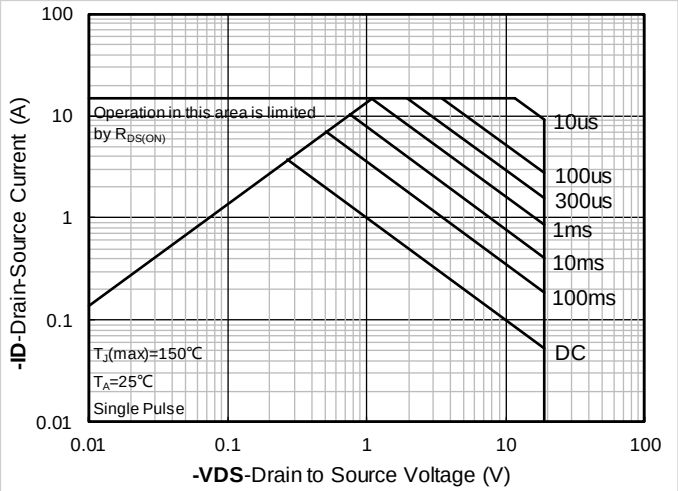


Figure7. Safe Operation Area

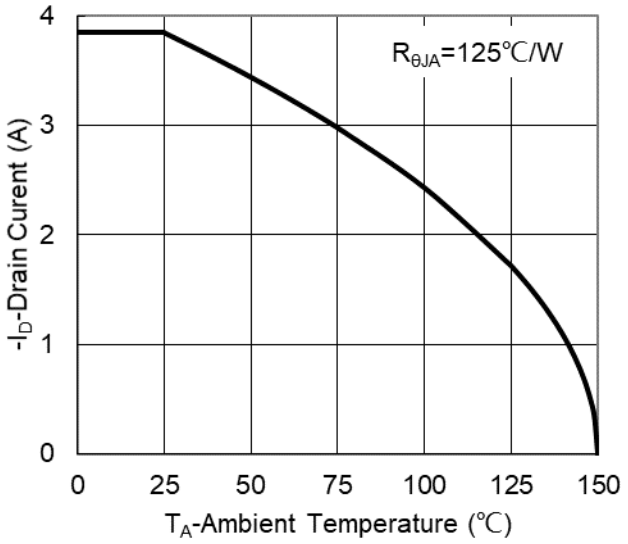


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

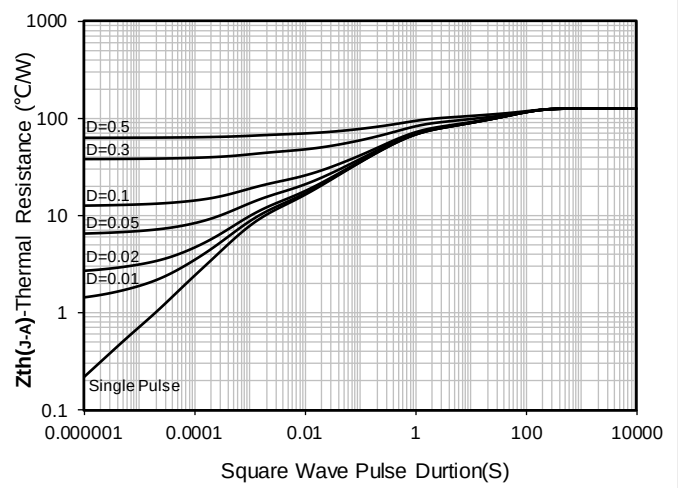
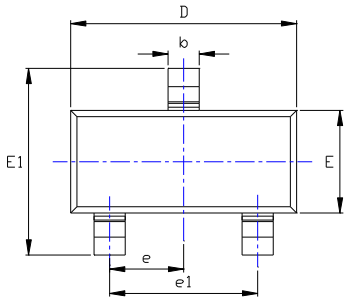
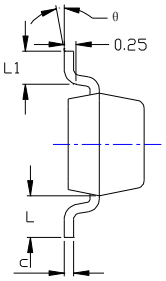


Figure9. Maximum Transient Thermal Impedance

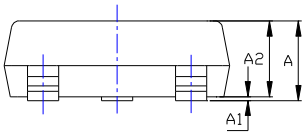
■ SOT-23 Package information



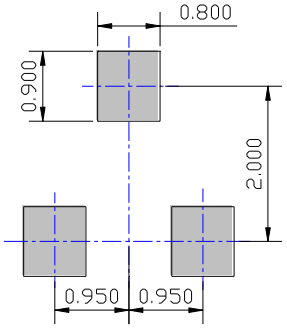
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT：mm

SUGGESTED SOLDER PAD LAYOUT

DIMENSIONS				
SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.045	0.900	1.150
A1	0.000	0.004	0.000	0.100
A2	0.035	0.041	0.900	1.050
b	0.012	0.020	0.300	0.500
c	0.004	0.008	0.100	0.200
D	0.110	0.118	2.800	3.000
E	0.047	0.055	1.200	1.400
E1	0.089	0.100	2.250	2.550
e	0.037TYP		0.950TYP	
e1	0.071	0.079	1.800	2.000
L	0.022REF		0.550REF	
L1	0.012	0.200	0.300	0.500
θ	0°	8°	0°	8°

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

Notice

All products, product specifications and data are subject to change without notice to improve reliability, function or design or otherwise.Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in STARSEA's terms and conditions of sale for such products, STARSEA assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of STARSEA products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.