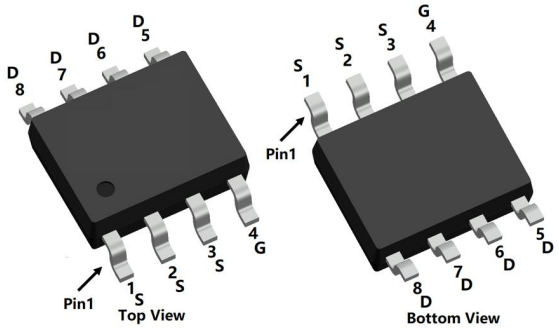
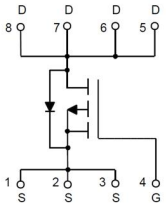
P-Channel Enhancement Mode Field Effect Transistor



SOP-8



Product Summary

- V_{DS} -60V
- I_D -5.9A
- $R_{DS(ON)}$(at V_{GS}=-10V) ＜47 mohm
- $R_{DS(ON)}$(at V_{GS}=-4.5V) ＜60 mohm
- 100% EAS Tested

General Description

- Split gate trench MOSFET technology
- High density cell design for low $R_{DS(ON)}$
- Low Crss
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Load switch
- Battery protection

■ Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	-60	V
Gate-source Voltage		V_{GS}	±20	V
Drain Current	T_A=25℃	I_D	-5.9	A
	T_A=100℃		-3.7	
Pulsed Drain Current [A]		I_{DM}	-30	A
Avalanche energy [B]		E_{AS}	81	mJ
Total Power Dissipation[C]	T_A=25℃	P_D	3.1	W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55～+150	℃

■Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient [D]	t≤10S	$R_{\theta JA}$	31	40	℃/W
Thermal Resistance Junction-to-Ambient [D]	Steady-State		59	75	
Thermal Resistance Junction-to-Lead	Steady-State	$R_{\theta JL}$	16	24	

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
S06GP06A	F2	Q06GP06A	4000	8000	64000	13“ reel

■ Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Conditions		Min	Typ	Max	Units
Static Parameter							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=-250μA		-60			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-60V,V_{GS}=0V	T_J=25℃			-1	μA
			T_J=55℃			-5	
Gate-Body Leakage Current	I_{GSS}	V_{GS}= ±20V, V_{DS}=0V				±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=-250μA		-1.3	-1.8	-2.5	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= -10V, I_D=-6A			35	47	mΩ
		V_{GS}= -4.5V, I_D=-3A			45	60	
Gate Resistance	Rg	f=1MHz			12		Ω
Diode Forward Voltage	V_{SD}	I_S=-6A,V_{GS}=0V			-0.85	-1.3	V
Maximum Body-Diode Continuous Current	I_S					-6.0	A
Dynamic Parameters							
Input Capacitance	C_{iss}	V_{DS}=-30V,V_{GS}=0V,f=1MHZ			1100		pF
Output Capacitance	C_{oss}				350		
Reverse Transfer Capacitance	C_{rss}				28		
Switching Parameters							
Total Gate Charge	$Q_{g(-10V)}$	V_{GS}=-10V,V_{DS}=-30V,I_D=-3A			18.7		nC
Total Gate Charge	$Q_{g(-4.5V)}$				8.8		
Gate-Source Charge	Q_{gs}				4.7		
Gate-Drain Charge	Q_{gd}				3.0		
Reverse Recovery Chrage	Q_{rr}	I_F=-3A, di/dt=100A/us			8.2		
Reverse Recovery Time	t_{rr}				20.2		ns
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=-10V,V_{DD}=-30V,I_{DS}=-3A R_{GEN}=6Ω			7.5		
Turn-on Rise Time	t_r				39.5		
Turn-off Delay Time	$t_{D(off)}$				43.6		
Turn-off fall Time	t_f				55.1		

A. Repetitive rating; pulse width limited by max. junction temperature.
B. V_{DD}=50V, R_G=25Ω, L=0.5mH, I_{AS}=18A.
C. Pd is based on max. junction temperature, using ≤10s junction-ambient thermal resistance.
D. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design.

■ Typical Performance Characteristics

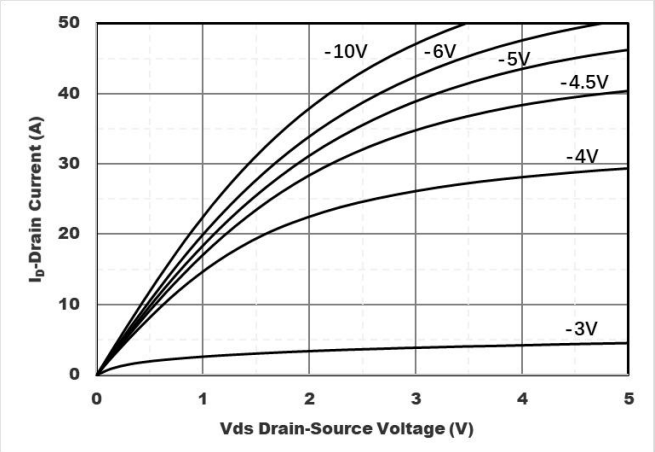


Figure1. Output Characteristics

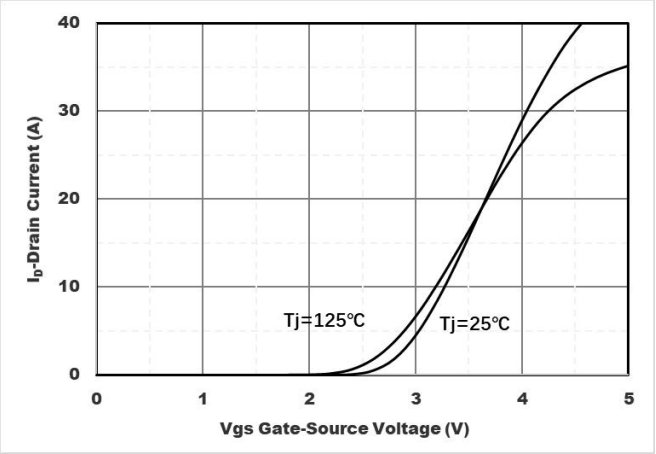


Figure2. Transfer Characteristics

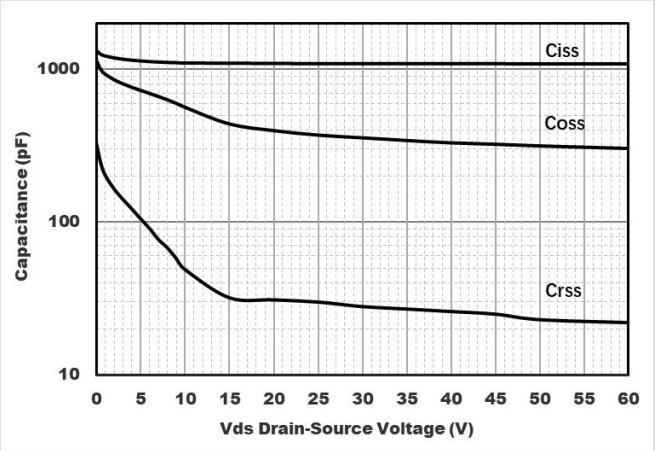


Figure3. Capacitance Characteristics

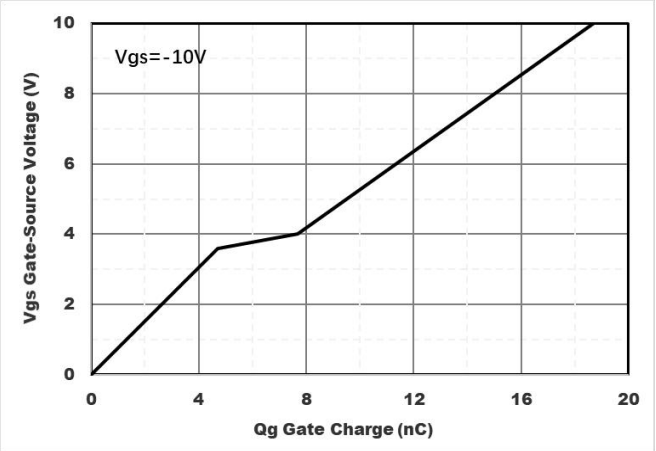


Figure4. Gate Charge

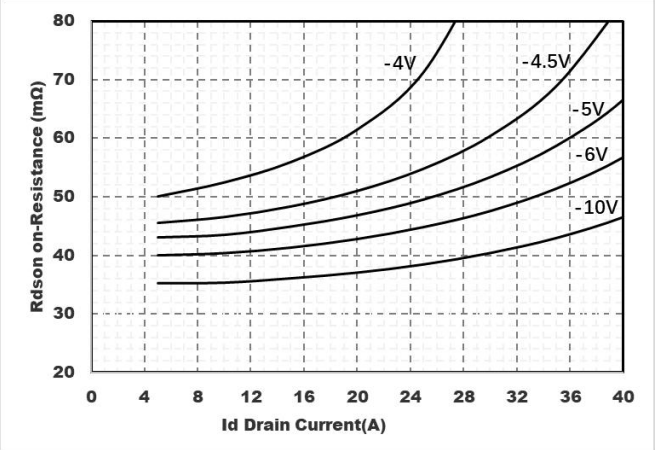


Figure5. : On-Resistance vs. Gate to Source Voltage

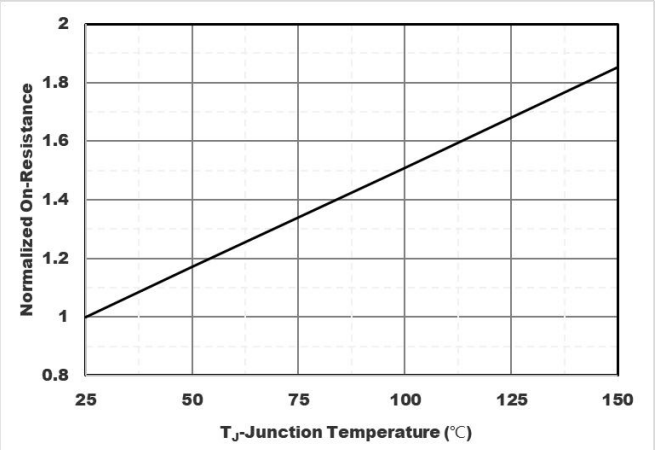


Figure6.Normalized On-Resistance

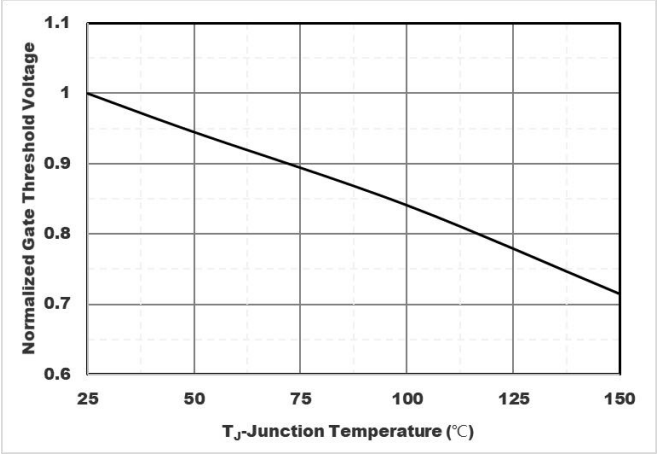


Figure7. Normalized Gate Threshold Voltage

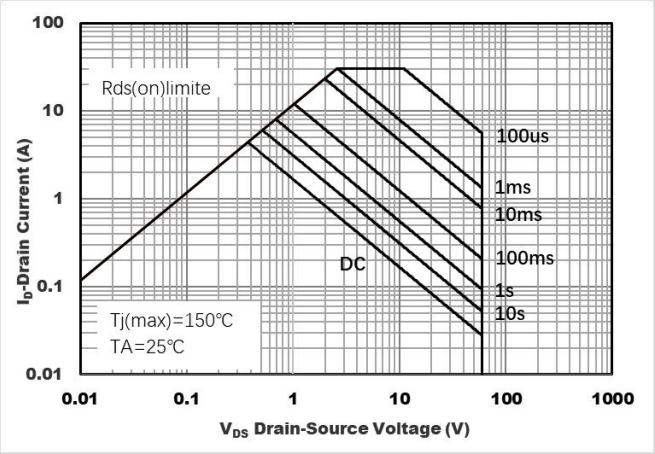


Figure8.Safe Operation Area

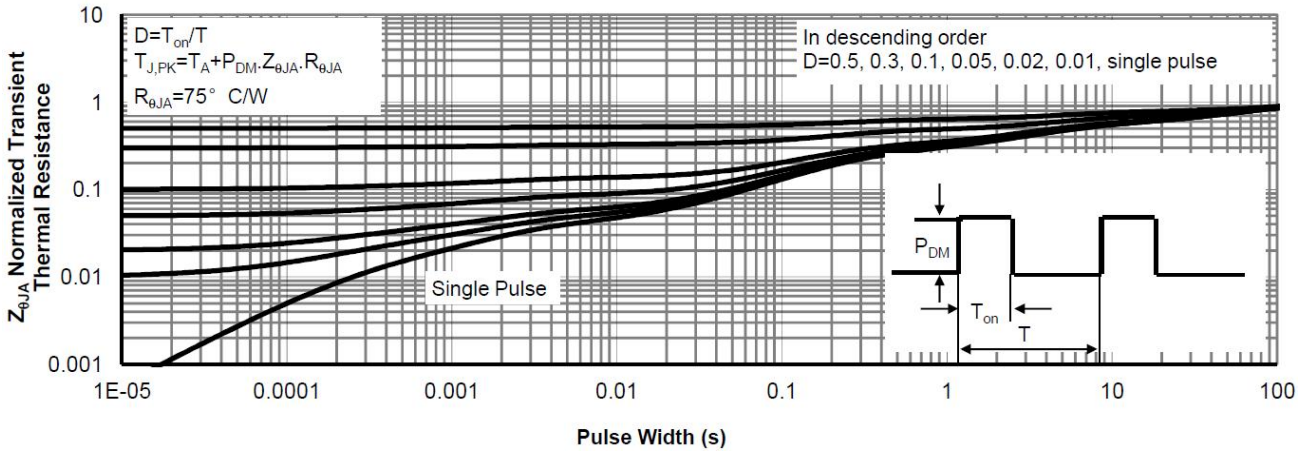


Figure9. Normalized Maximum Transient Thermal Impedance

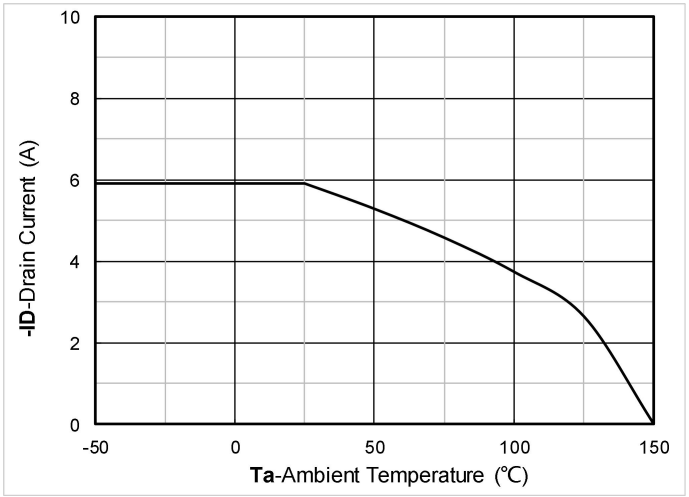
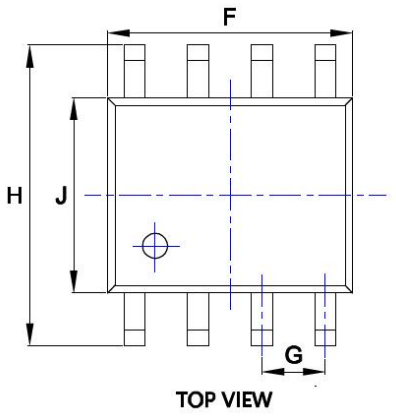
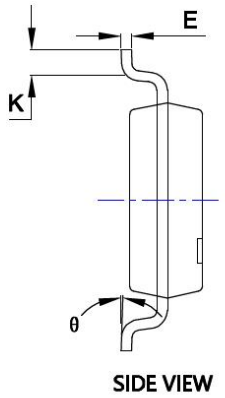


Figure 10. Current dissipation

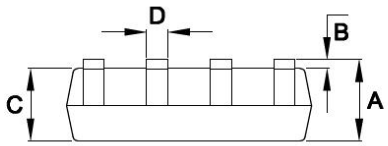
■ SOP-8 Package information



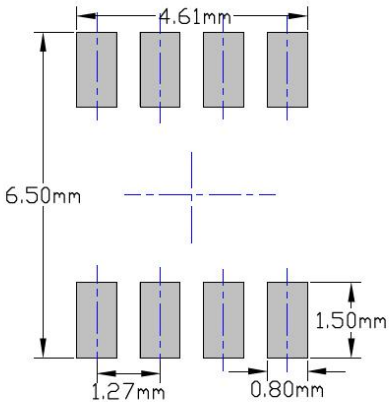
TOP VIEW



SIDE VIEW



SIDE VIEW



Suggested Solder Pad Layout

DIMENSIONS				
SYMBOL	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.053	0.069	1.350	1.750
B	0.004	0.010	0.100	0.250
C	0.053	0.061	1.350	1.550
D	0.013	0.020	0.330	0.510
E	0.007	0.010	0.170	0.250
F	0.189	0.197	4.800	5.000
G	0.050BSC		1.270BSC	
H	0.228	0.244	5.800	6.200
J	0.150	0.157	3.800	4.000
K	0.016	0.050	0.400	1.270
θ	0°	8°	0°	8°

Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

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