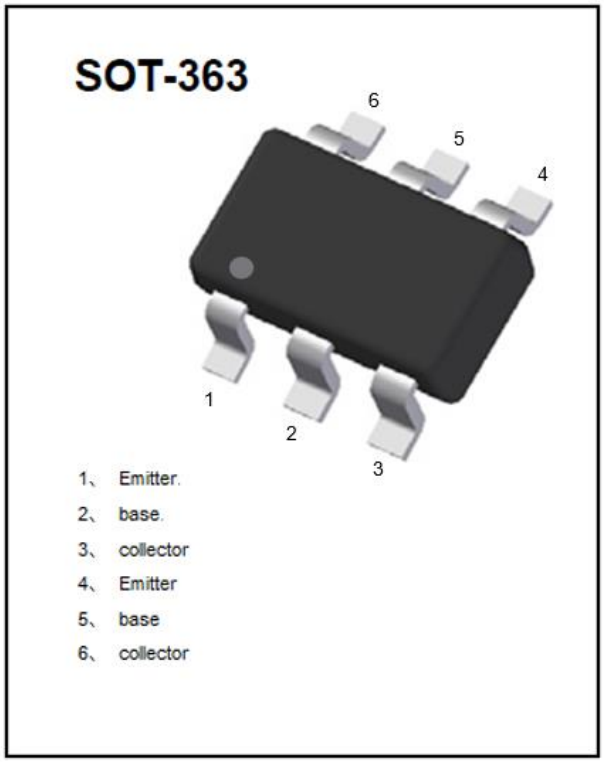
Dual PNP Small Signal Transistor



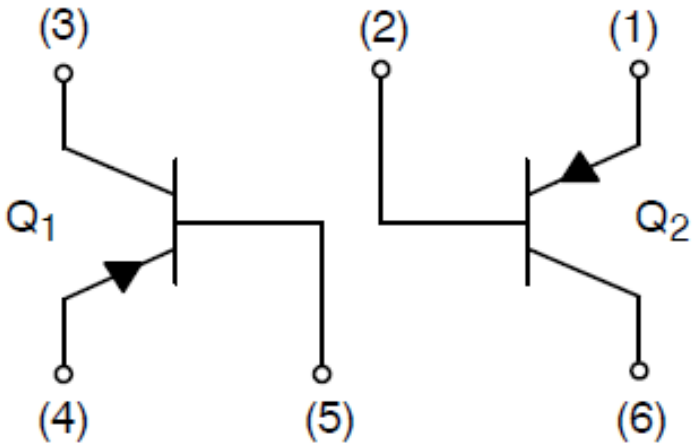
Features

- Epoxy meets UL-94 V-0 flammability rating
- Surface mount package ideally Suited for Automatic Insertion
- PNP

Mechanical Data

- **Package:** SOT-363
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** 5Ft

■Equivalent circuit



■Maximum Ratings (Ta=25℃ Unless otherwise specified）

Item	Symbol	Unit	Conditions	Value
Collector-Base Voltage	VCBO	V	IC=-10μA,IE=0	-80
Collector-Emitter Voltage	VCEO	V	IC =-10mA,IB=0	-65
Emitter-Base Voltage	VEBO	V	IE=-10μA,IC=0	-5
Collector Current	IC	mA		-100
Total Device Dissipation	PC	mW		200
Thermal resistance, junction-to-ambient	$R_{\theta J\text{-}A}$(1)	℃/W		625
Junction Temperature	Tj	℃		-55 to +150
Storage Temperature	TSTG	℃		-55 to +150

Note:
（1）Device mounted on PCB, single-sided copper，with standard footprint

■Electrical Characteristics (Ta=25℃ unless otherwise specified)

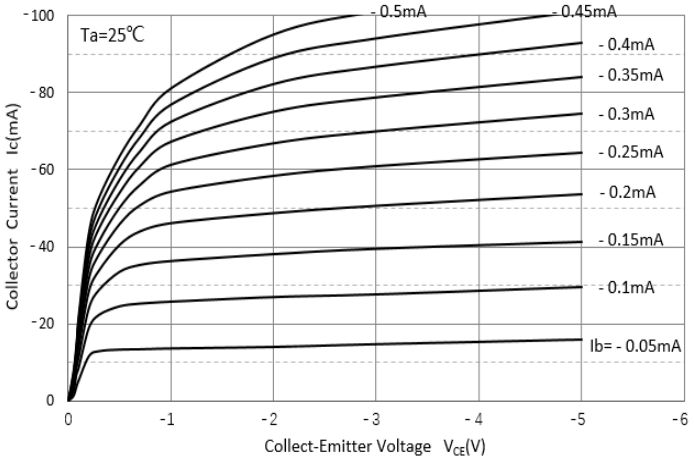
Item	Symbol	Unit	Conditions	Min	TYP	Max
Collector-base breakdown voltage	$V_{(BR)CBO}$	V	IC=-10μA,IE=0	-80		
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	V	IC =-10mA,IB=0	-65		
Emitter-base breakdown voltage	$V_{(BR)EBO}$	V	IE=-10μA,IC=0	-5		
Collector-Base cut-off current	I_{CBO}	nA	VCB=-30V,IB=0			-15
Emitter-Base cut-off current	I_{EBO}	nA	VEB=-5V,IC=0			-100
DC current gain	h_{FE}		VCE=-5V,IC=-2mA	110		
Collector-emitter saturation voltage	$V_{CE(sat)}$	V	IC=-10mA,IB=-0.5mA			-0.3
Collector-emitter saturation voltage	$V_{CE(sat)}$	V	IC=-100mA,IB=-5mA			-0.65
Base-emitter saturation voltage	$V_{BE(sat)}$	V	IC=-10mA,IB=-0.5mA			-0.85
Base-emitter Voltage	VBE	V	VCE=-5V,IC=-2mA	-0.6		-0.75
Transition frequency	Ft	MHz	VCE=-5V,IC=-10mA,f=100MHz	100		

■ Ordering Information (Example)

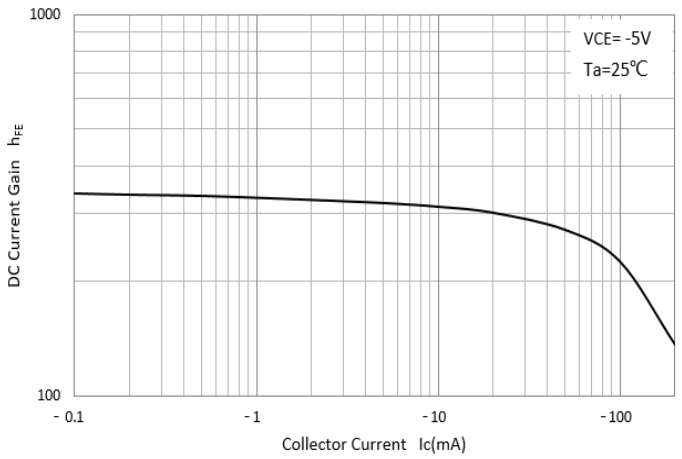
PREFERED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
BC856S	F2	Approximate 0.009g	3000	30000	120000	7" reel

■ Characteristics (Typical)

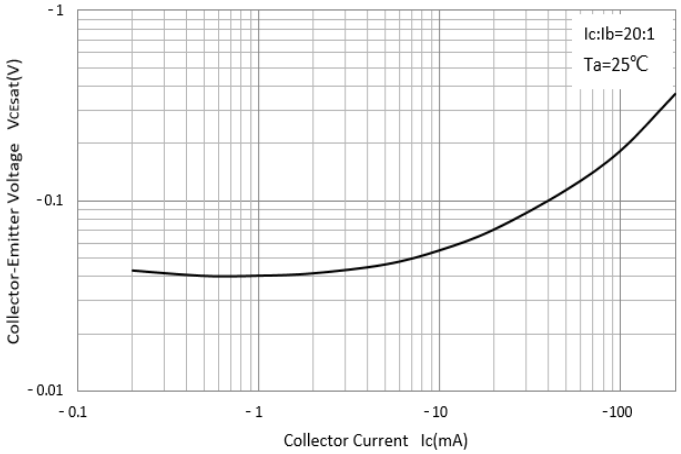
Static Characteristic



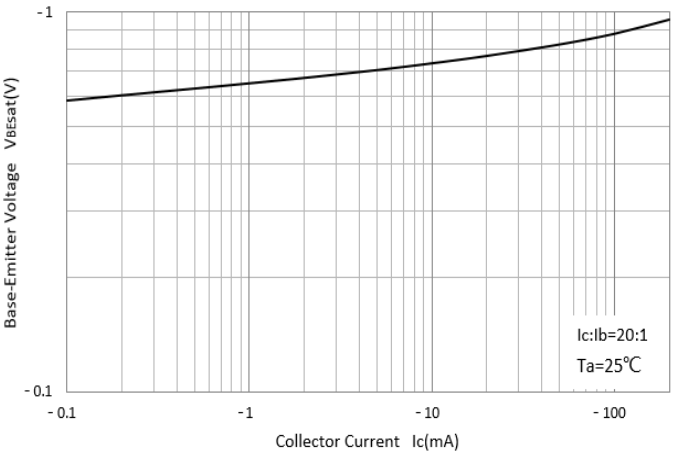
DC Current Gain



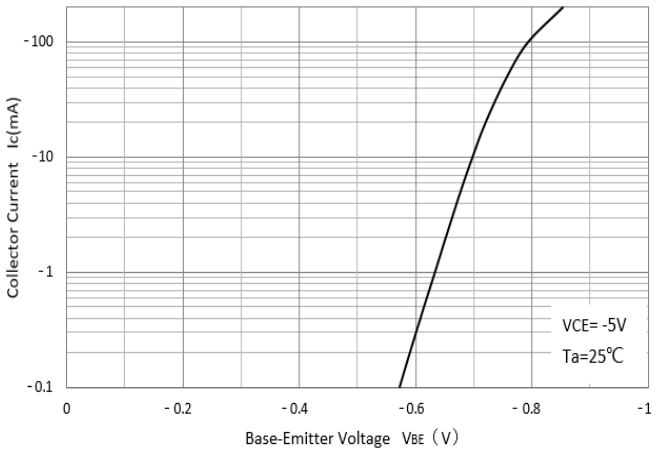
Collector-Emitter Saturation Voltage



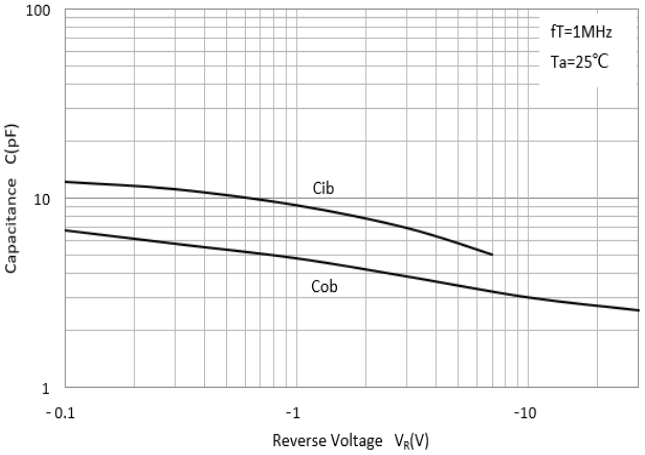
Base-Emitter Saturation Voltage



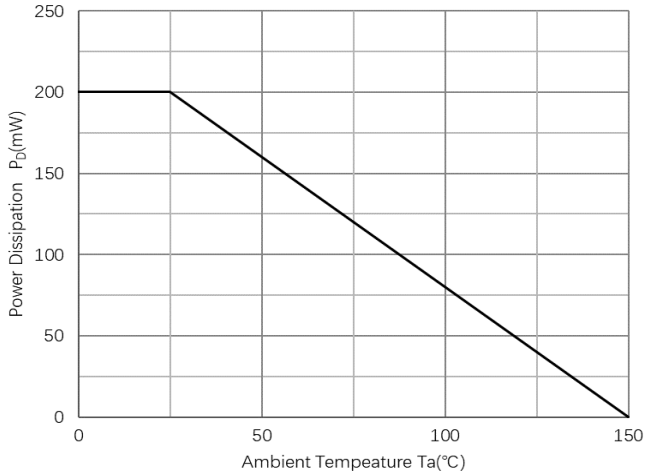
Base-Emitter On Voltage



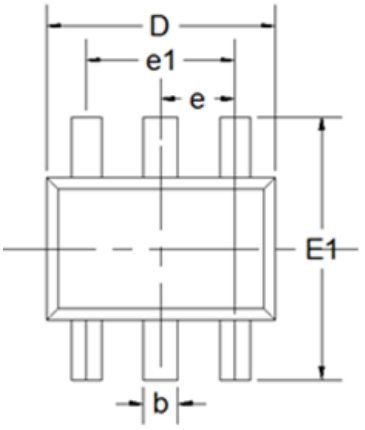
Cob/Cib-V_{CB}/V_{EB}



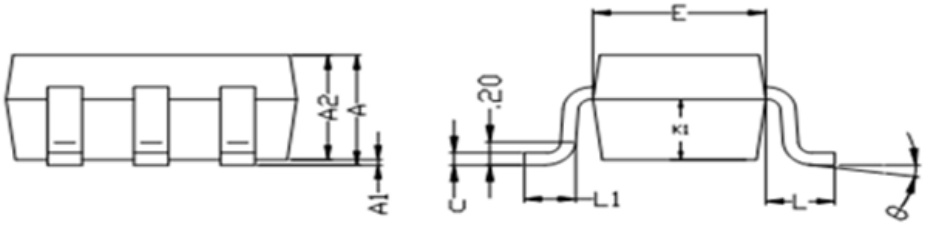
Power Derating Curve



■SOT-363 Package Outline Dimensions

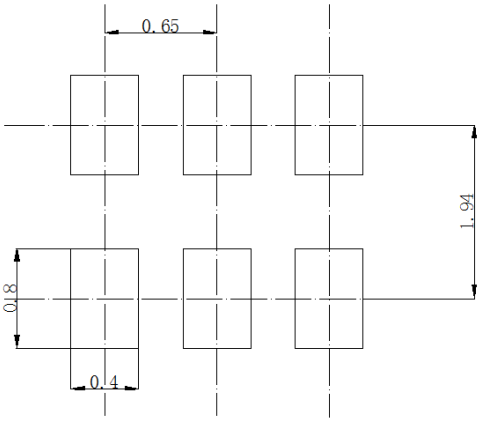


DIM	DIMENSIONS			
	INCHES		MM	
	MIN	MAX	MIN	MAX
A	0.035	0.043	0.9	1.1
A1	0	0.004	0	0.1
A2	0.035	0.039	0.9	1
b	0.006	0.014	0.15	0.35
c	0.002	0.01	0.05	0.25
D	0.071	0.087	1.8	2.2
E	0.045	0.053	1.15	1.35
E1	0.085	0.096	2.15	2.45
e	0.026Typ		0.65Typ	
e1	0.047	0.055	1.2	1.4
L	0.021Typ		0.525Typ	
L1	0.01	0.018	0.26	0.46
θ	0°	8°	0°	8°



Note:
1. All dimensions are in millimeters (mm) unless otherwise specified.
[所有尺寸均以毫米为单位，除非另有说明]
2. General tolerances: ±0.10mm unless otherwise specified.
[通用公差为±0.10mm，除非另有说明]
3. Dimensions and tolerances per ASME Y14.5M-2018.
[尺寸和公差遵循 ASME Y14.5M-2018 标准]
4.All dimensions shown are exclusive of burrs and gate residues.
Burrs and gate vestiges shall not exceed 0.15 mm in maximum.
[所有尺寸均不包括毛刺和浇口残留。毛刺与浇口残留的尺寸最大不得超过 0.15mm]
5.Dimension b does not include dambar protrusion of max 0.100 mm per side.
[尺寸b不包括单边最大0.100 MM的中筋凸出部分]
6.Dimensions D and E are the overall extreme outer dimensions of the mold compound. These dimensions exclude mold flash, lead flash, protrusions and burrs but include the maximum allowable mold mismatch.
[D和E是塑封体的外部极限尺寸，不包括包封溢料、内引线溢料、凸出部分以及胶体毛刺，但是包含了包封错位的最大尺寸]
7.Formed leads shall be planar with respect to one another within a maximum of 0.076 mm relative to the seating plane.
[成型的管脚应为同一平面，共面性最大为0.1mm]

■SOT-363 Suggested Pad Layout



Unit：mm

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